

Abstract of the Disclosure:

A sigma-delta programmer is supplied with a data word having a word length of N bits. The most significant L bits of the data word represent the places before the decimal point, and
5 the remaining $N-L$ less significant bits represent the places after the decimal point in the data word. A sigma-delta modulator is supplied with the $N-L+1$ less significant bits of the data word. An adder receives the $L-1$ most significant bits of the data word and a data word that is output by the
10 sigma-delta modulator, and outputs a signal, which is multiplied by the value two by a multiplier.

LDP/nt